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|---------------------|--------------------------|
| 1 Vref Selection | 6 MOSI (Board) Selection |
| 2 Vcc Selection | 7 SCK Selection |
| 3 I/O Pins | 8 MISO (Board) Selection |
| 4 Optional buffer | 9 CS Selection |
| 5 Buffered I/O Pins | |

1 Vref Selection. If open Vref pin is connected to GND through 100nF capacitor. I closed connected to 3.3V

2 Vcc Selection. Should be left in the pictured position (3.3V). **5V Vcc is an experimental feature and it can possibly damage your PI !!!**

4 Optional buffer. Optional OpAmp in the voltage follower configuration. Buffered I/O pins are only connected if this amplifier is installed.

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|--------------------------|---|
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In pictured position SPI0. In opposite SPI1.
 SPI1 can be only used if you have upgraded Raspbian to minimum v4.6. Run `sudo raspi-update`.
 SPI0 or/and SPI1 have to enabled before use.
 Another method is to use our low level userspace SPI library.